

Fig. 1

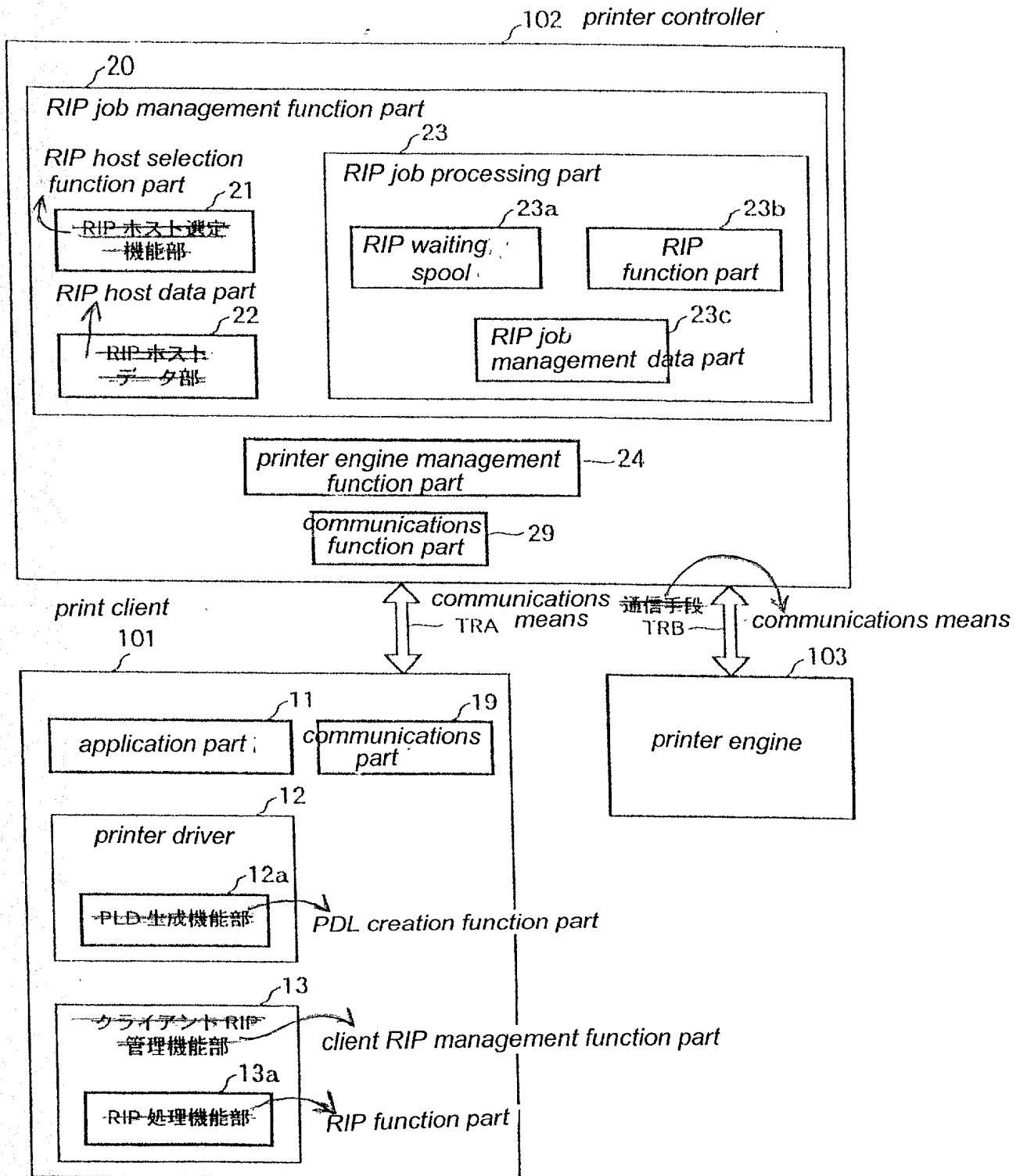


Fig. 2

	RIP permission
printer controller	present
print client	present

Fig. 3

job number ← ジョブ番号	← 処理順 → processing order
J B a	3
J B b	1
J B c	2
⋮	⋮
J B n	n - 1

Fig. 4

$x = (\text{total number of pages of all PDL in RIP spool}) + \text{number of pages of PDL freshly required of RIP}$

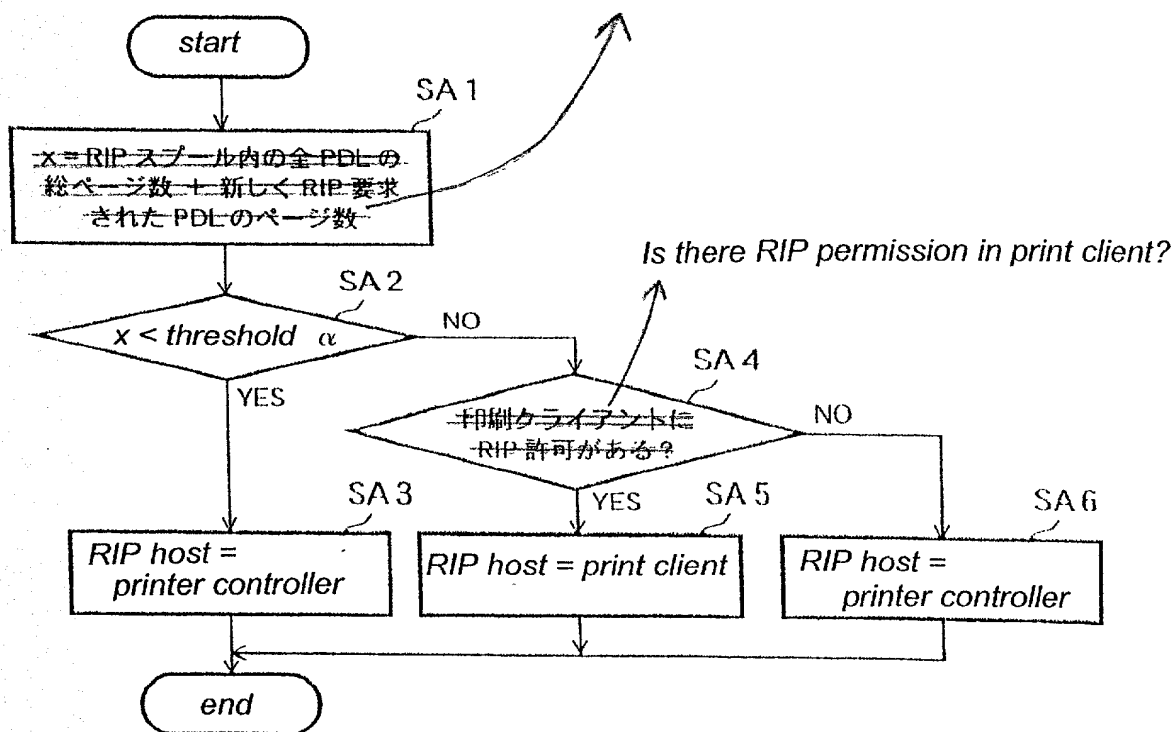


Fig. 5

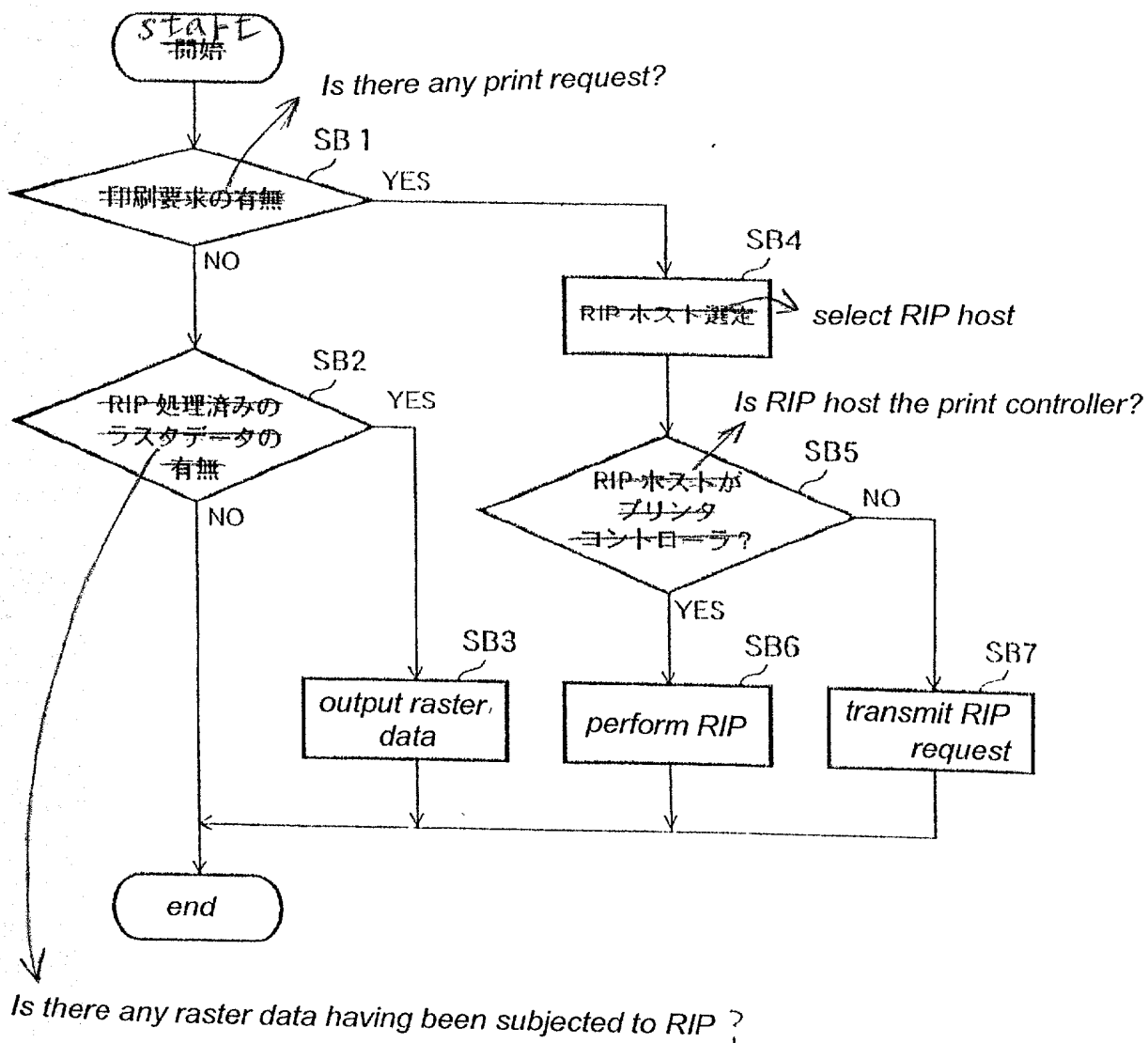


Fig. 6

2020-05-20 09:05:00

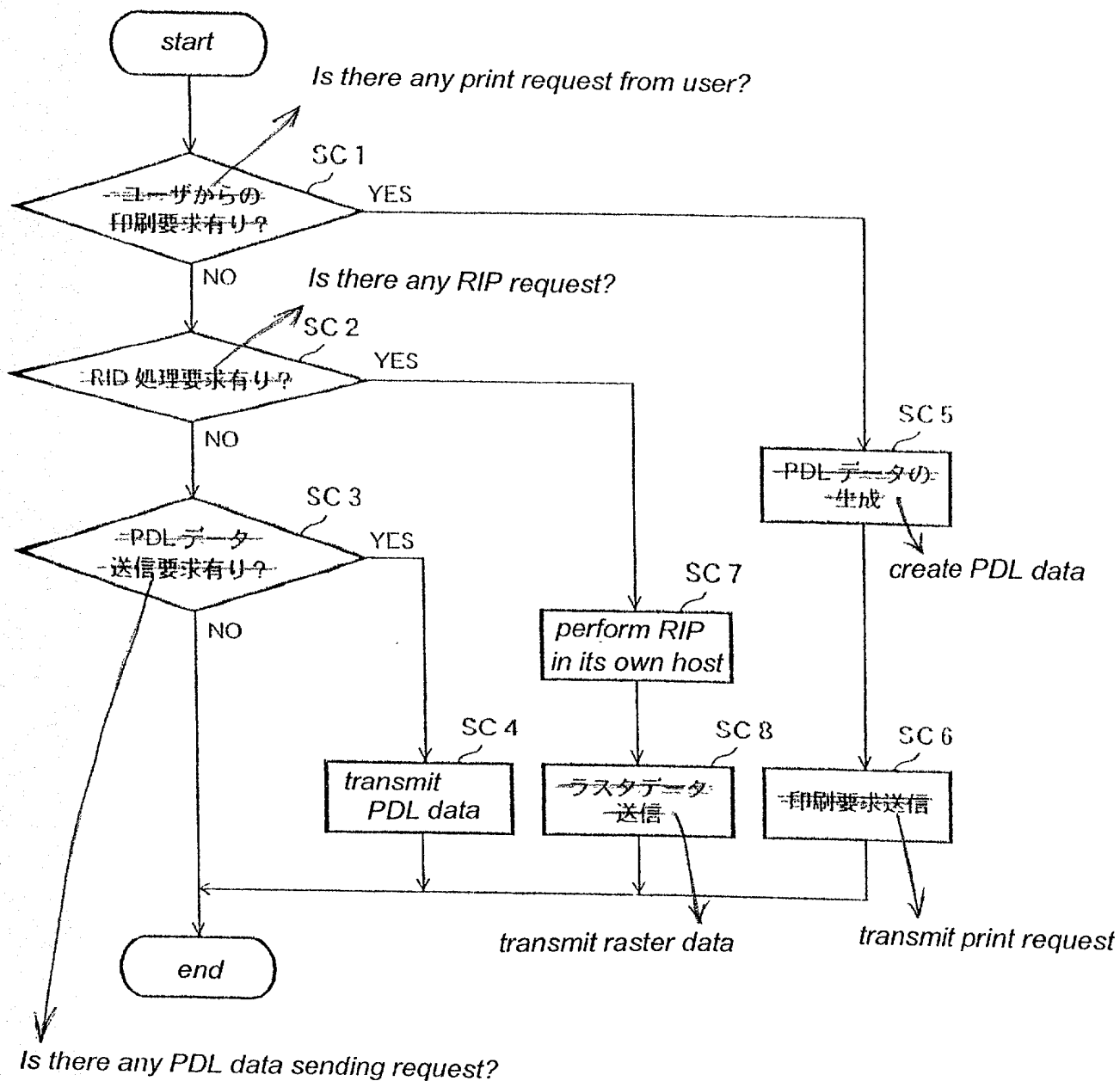


Fig. 7

client RIP management function part

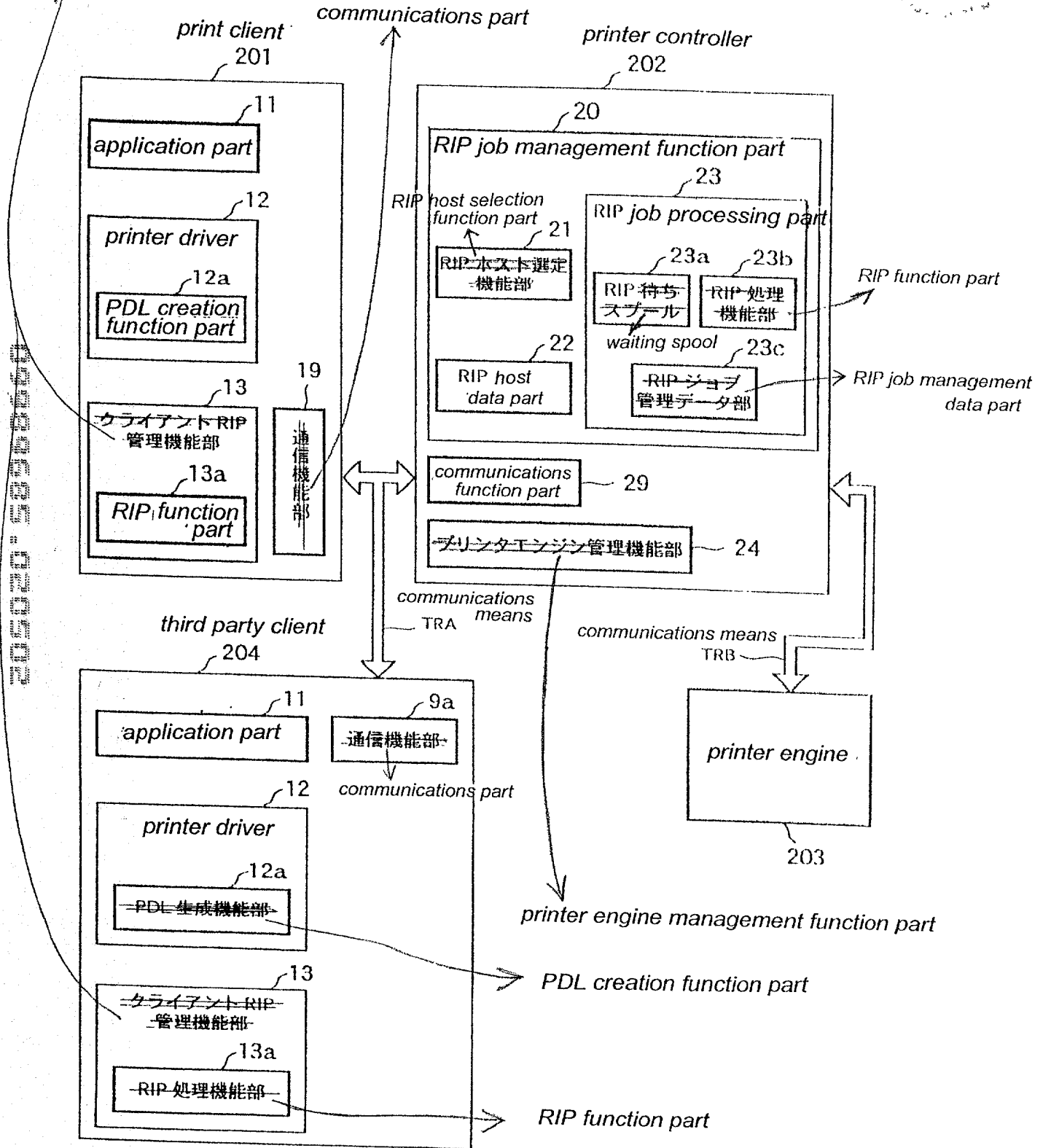


Fig. 8

CPU clock frequency
↑

	RIP permission - CPUクロック	Memory
printer controller	present 800MHz	2GB
print client	present 1.5GHz	512MB
third party client	present 900MHz	128MB

2020-05-05 09:00

[illegible]

△

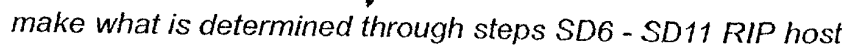
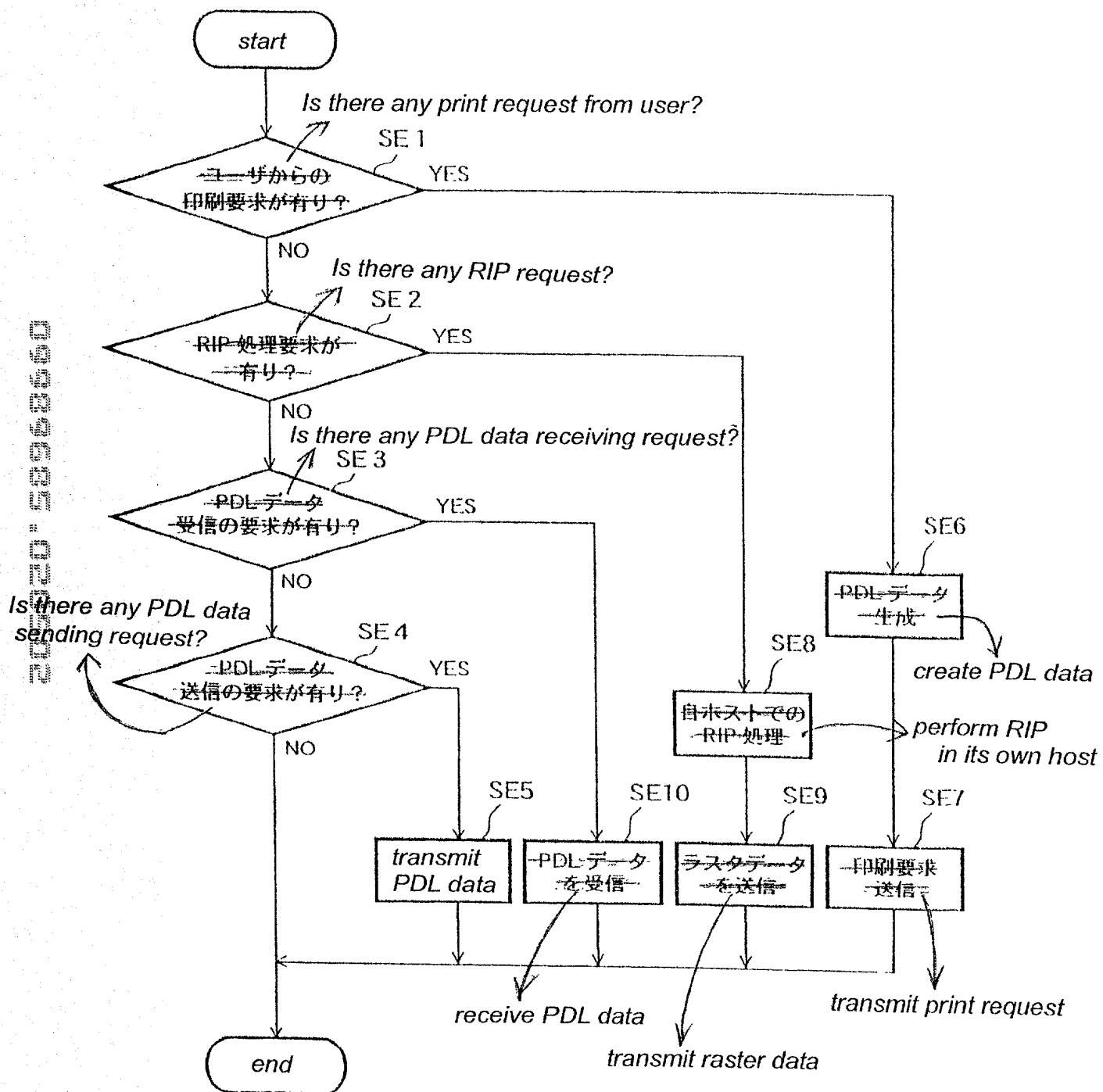


Fig. 10



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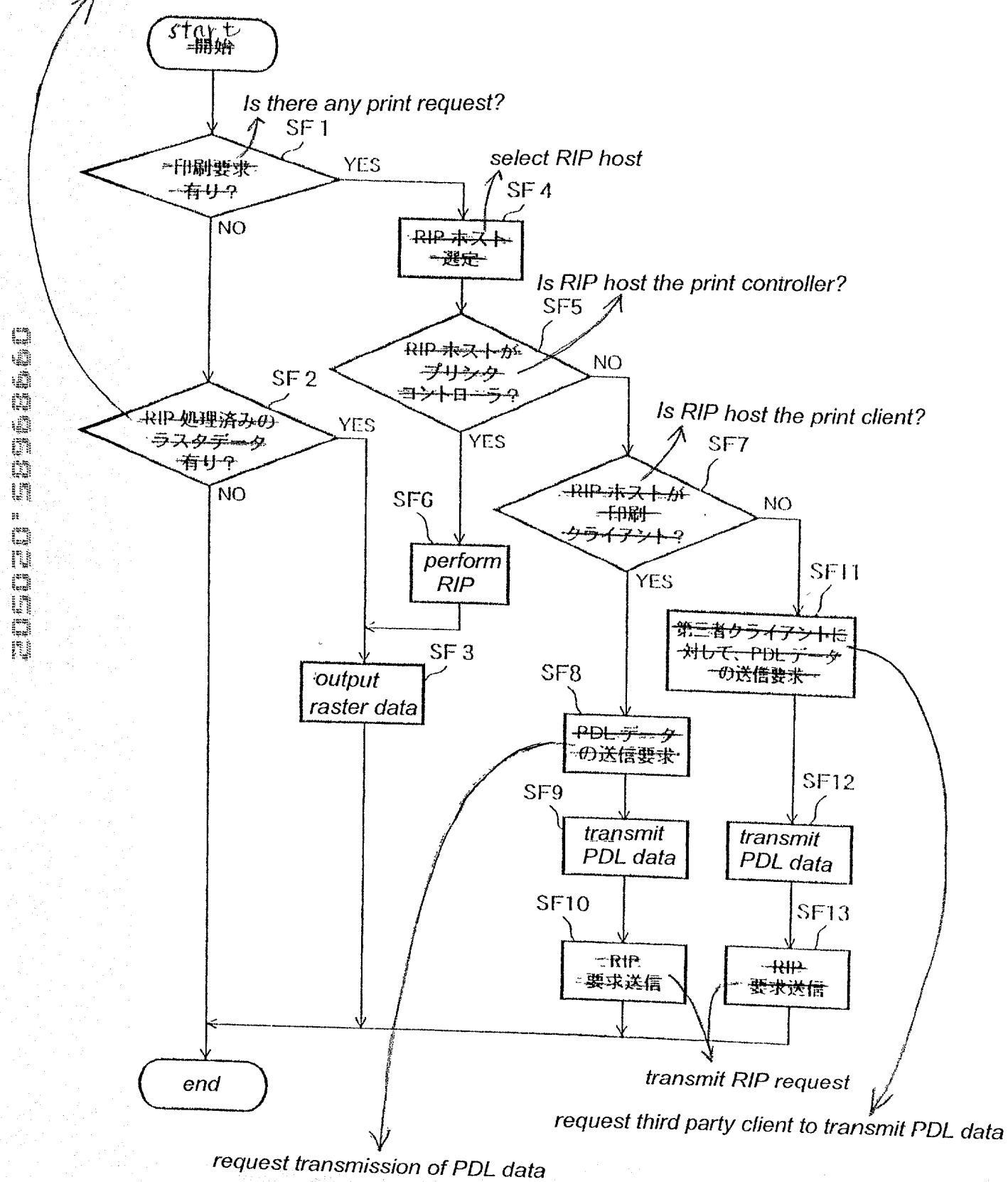


Fig. 12

RIP host selection function part

RIP job management data part

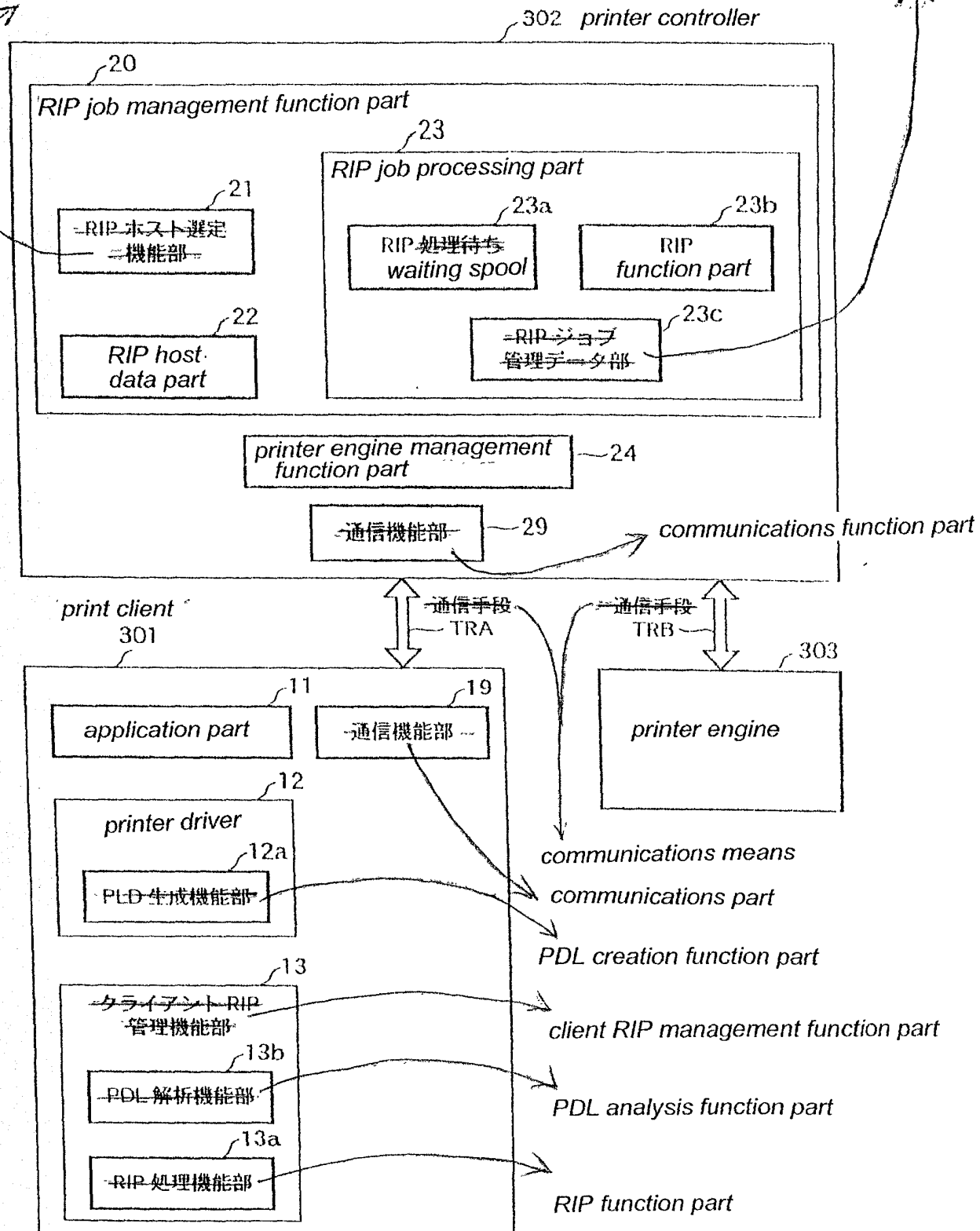


Fig. 13

job number	RIP許可
printer controller	present
print client	present

RIP permission

Fig. 14

job number	処理順	ページ数
J B a	2	54
J B b	1	79

processing order number of pages

Fig. 15

$$x = (\text{total number of pages of all PDL in RIP spool}) + (\text{number of pages of PDL freshly required of RIP})$$

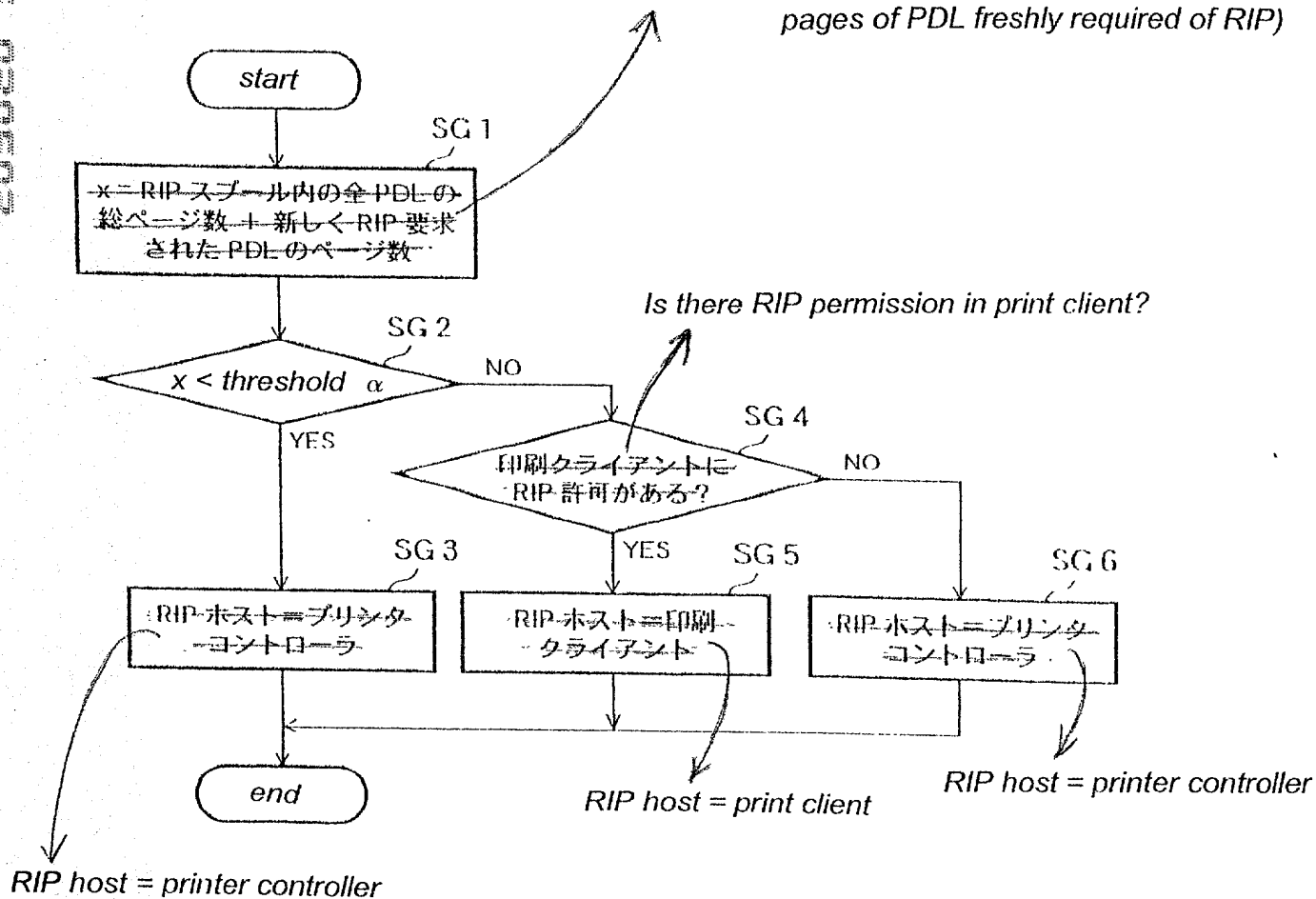
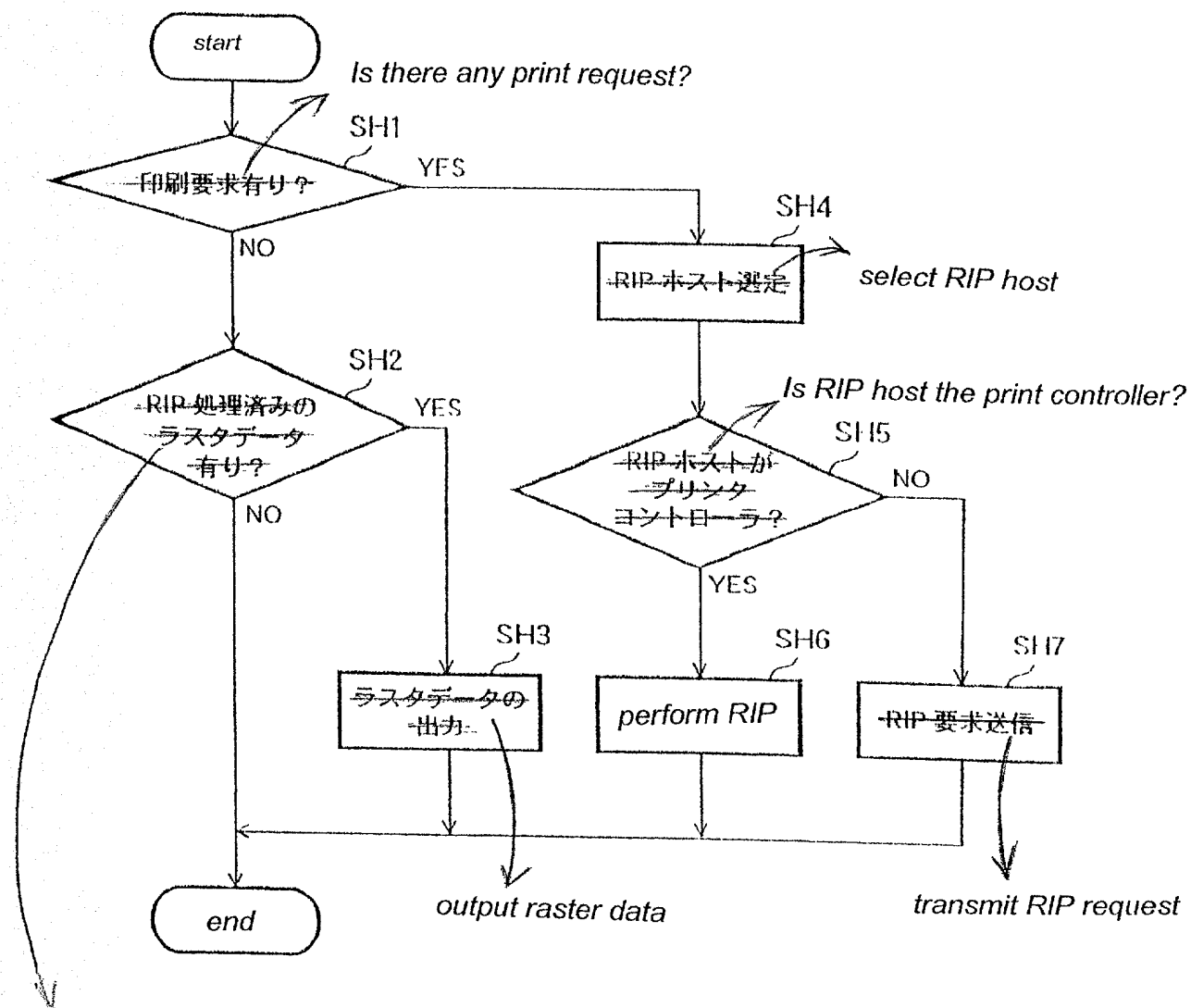


Fig. 16

09909685-020502



Is there any raster data having been subjected to RIP?

Fig. 17

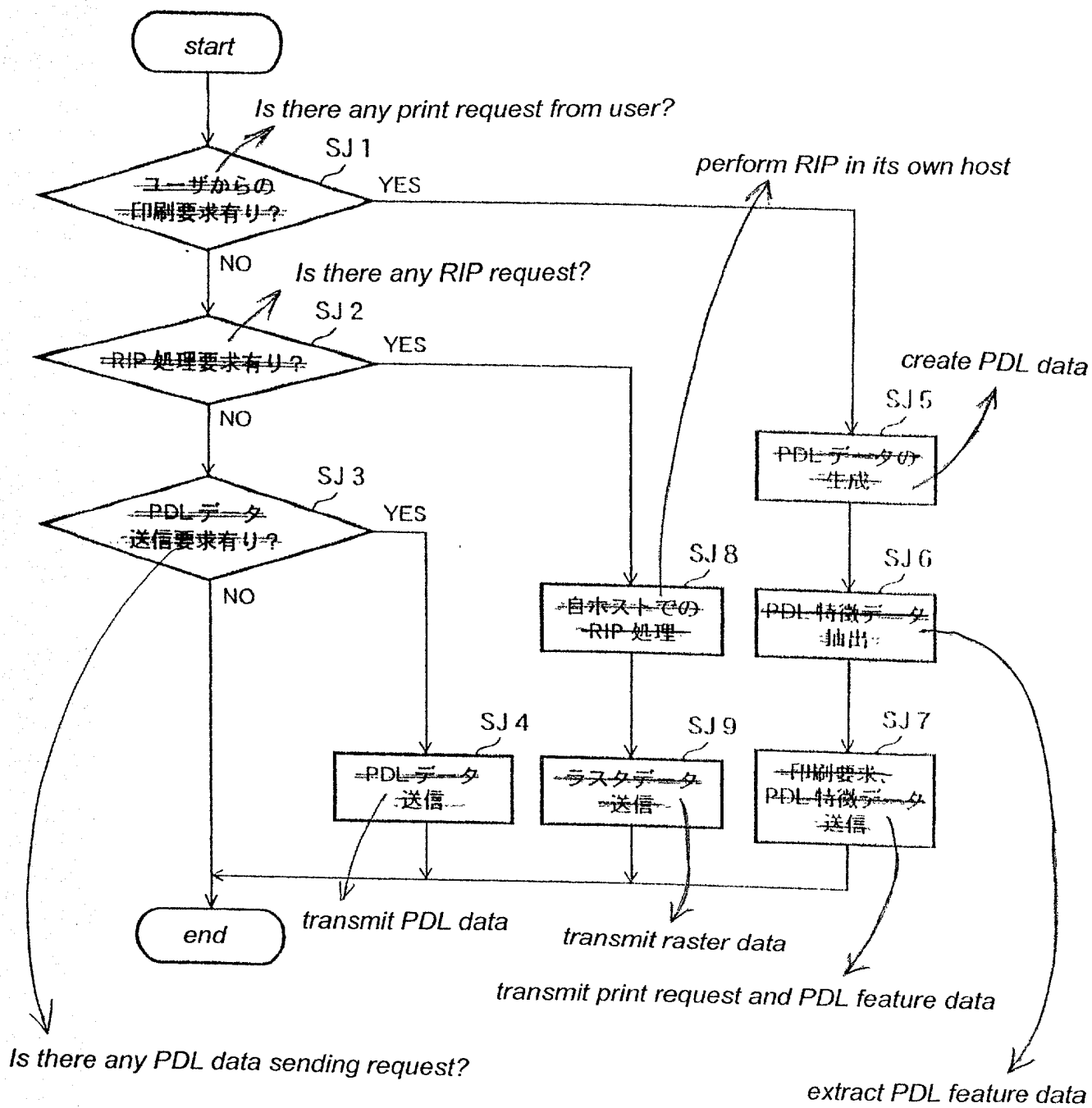


Fig. 18

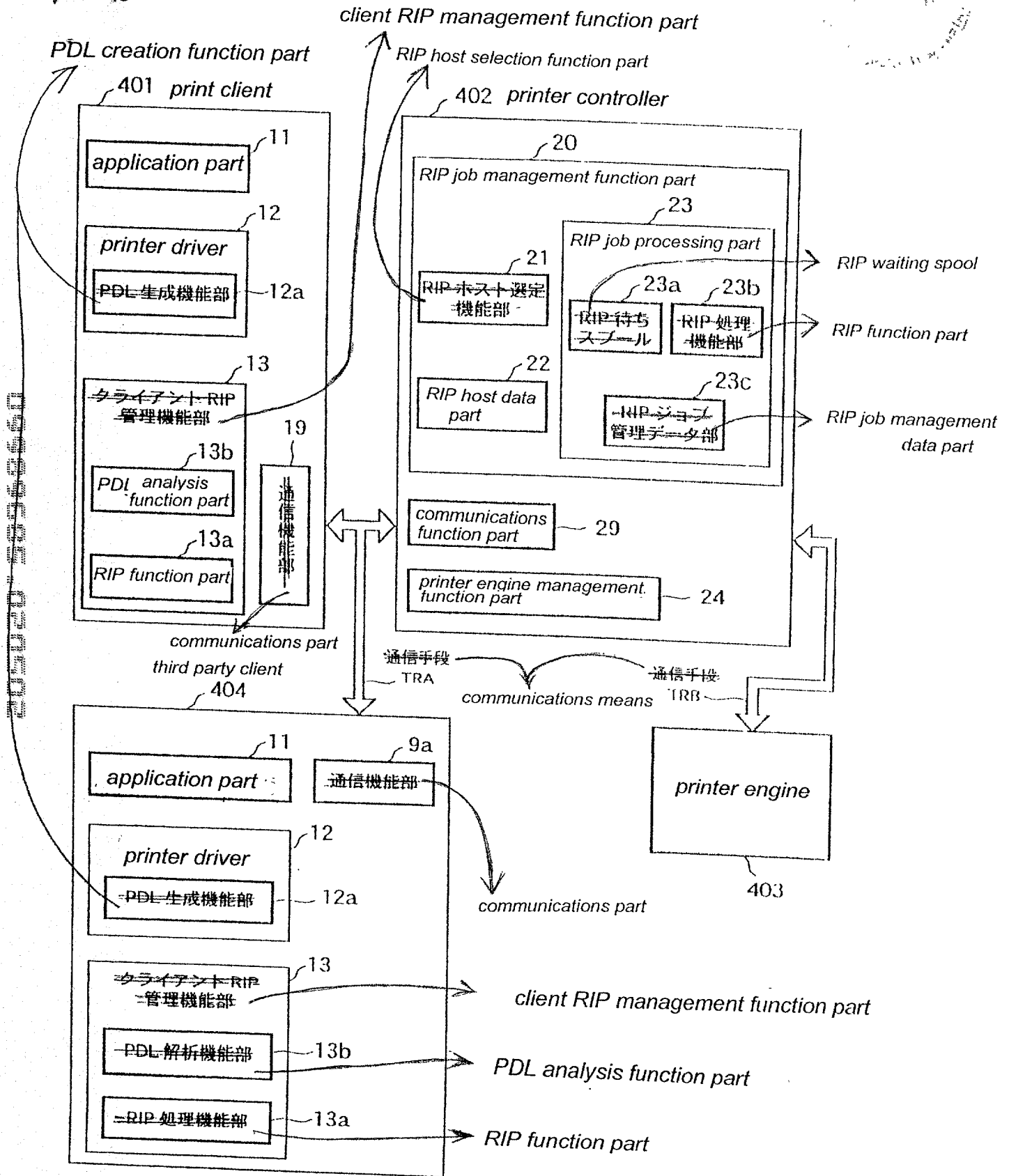


Fig. 19 printer controller

print client

third party client

CPU clock frequency

RIP permission

	RIP 処理の許可	CPU クロック	Memory
プリンタコントローラ	present	800MHz	2GB
印刷クライアント	present	1.5GHz	512MB
第三者クライアント	present	900MHz	128MB

Fig. 20

job number	processing order
J B a	3
J B b	1
J B c	2
⋮	⋮
J B n	n - 1

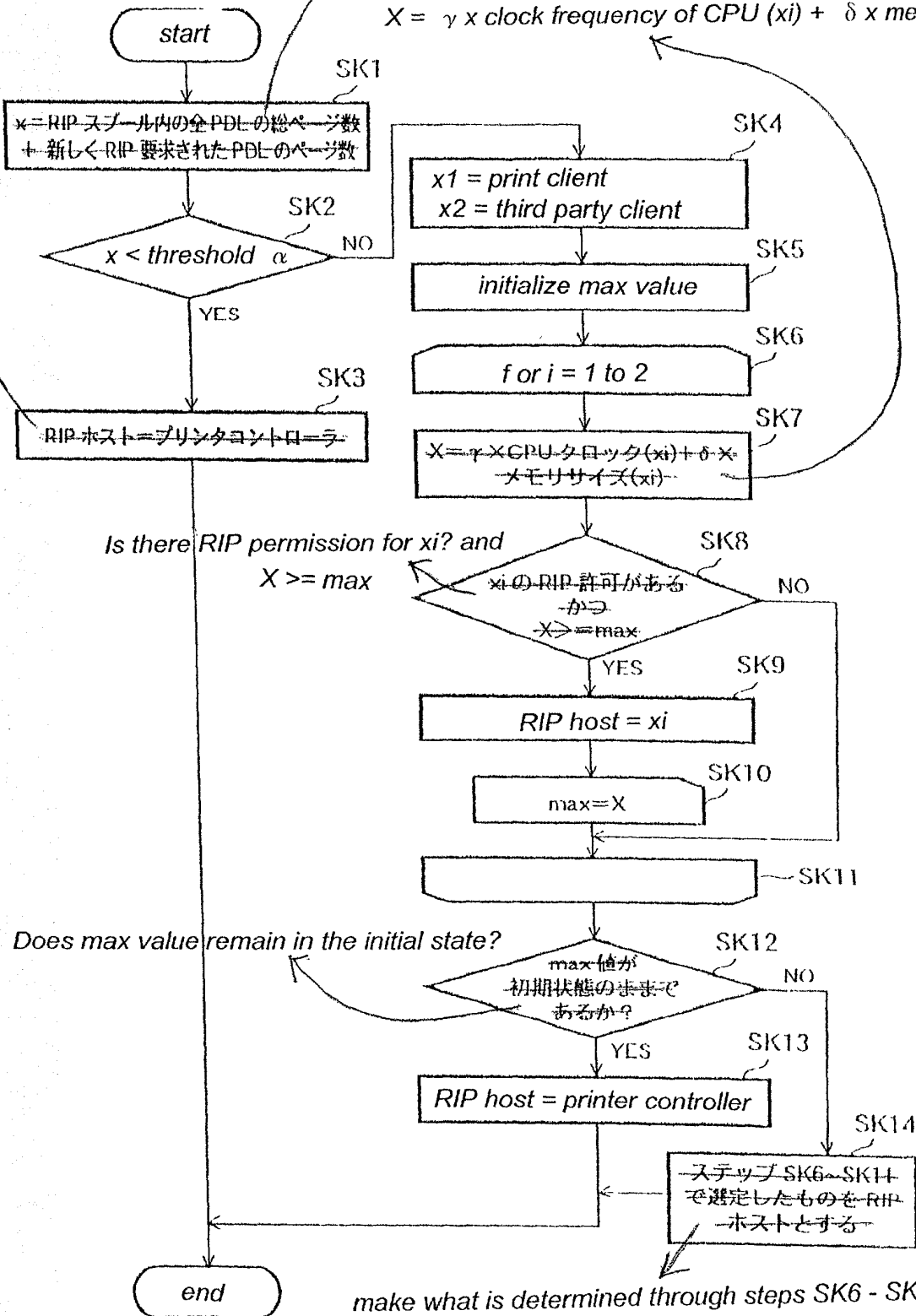
205020-52988660

RIP host = printer controller

Fig. 21

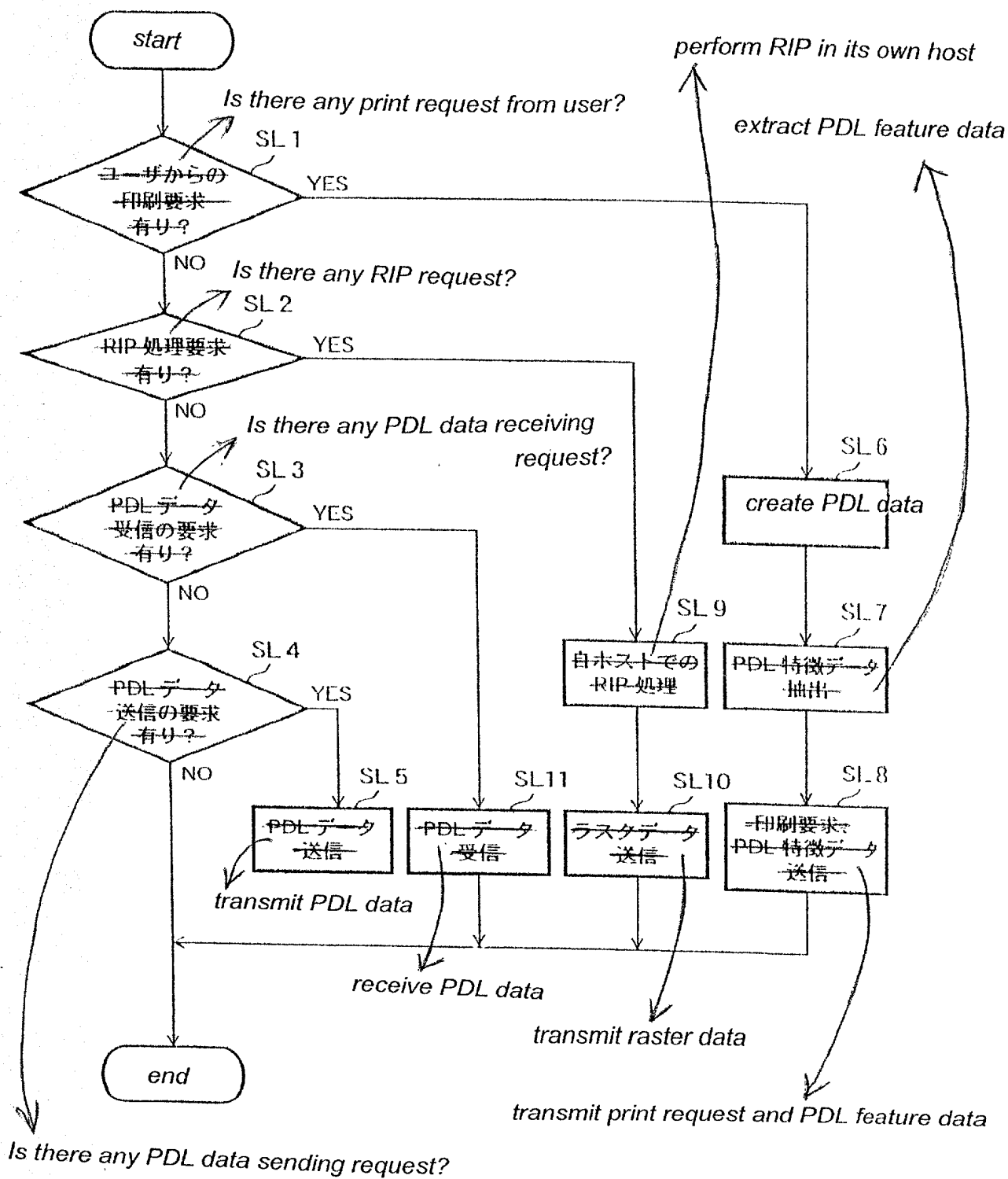
$x = (\text{total number of pages of all PDL in RIP spool}) + (\text{number of pages of PDL freshly required of RIP})$

$X = \gamma \times \text{clock frequency of CPU } (x_i) + \delta \times \text{memory size } (x_i)$



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Fig. 22



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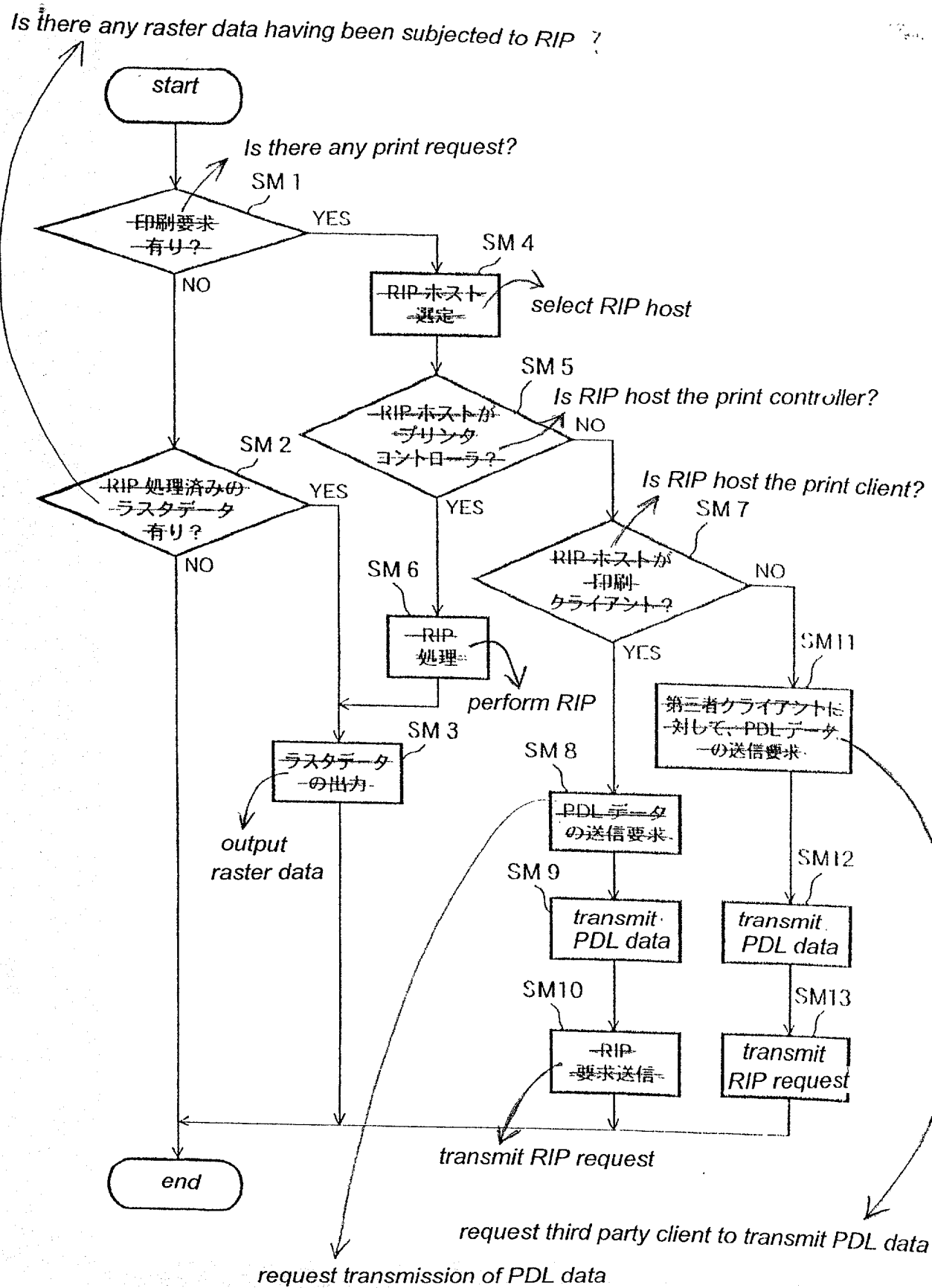


Fig. 24

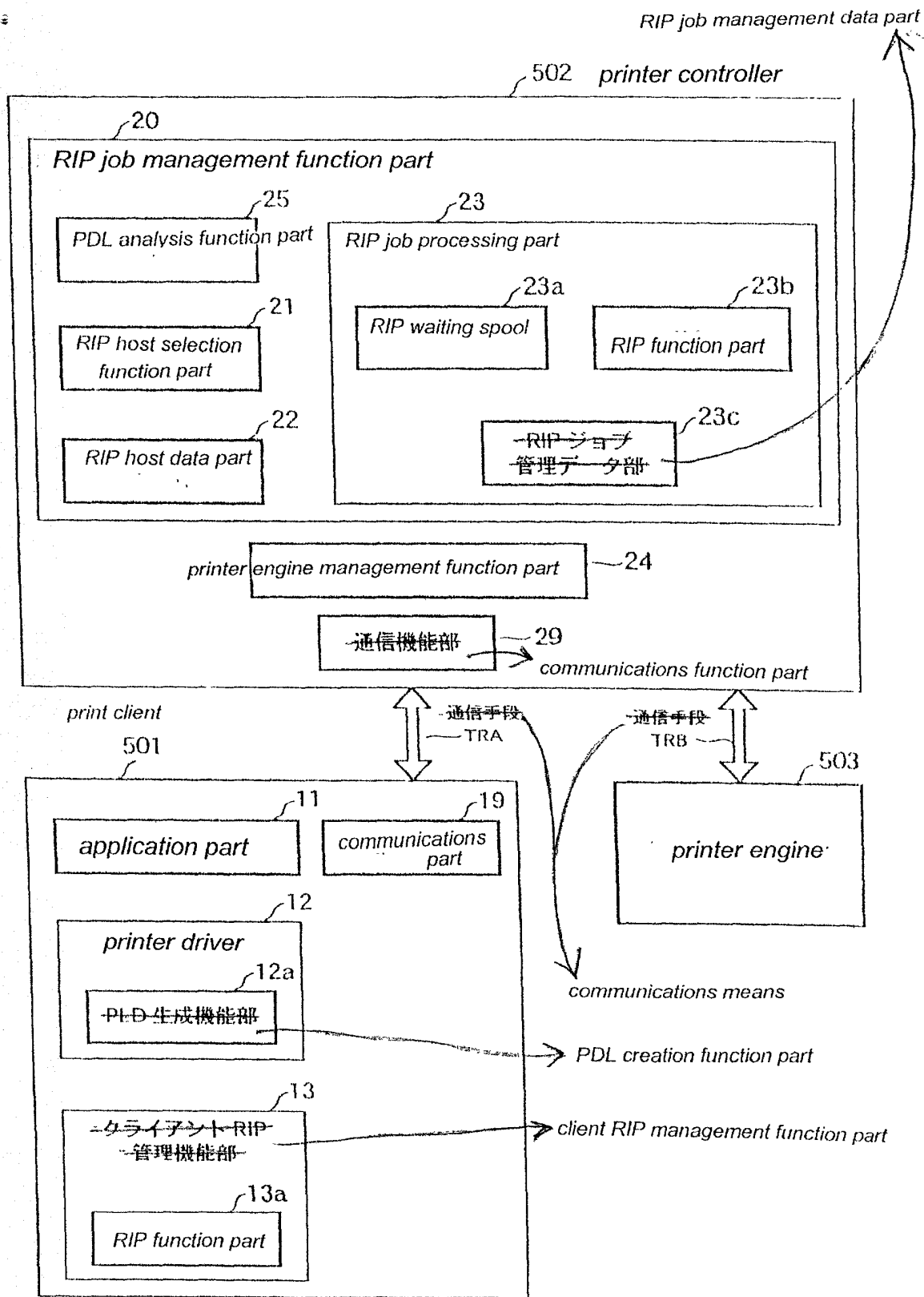


Fig. 25

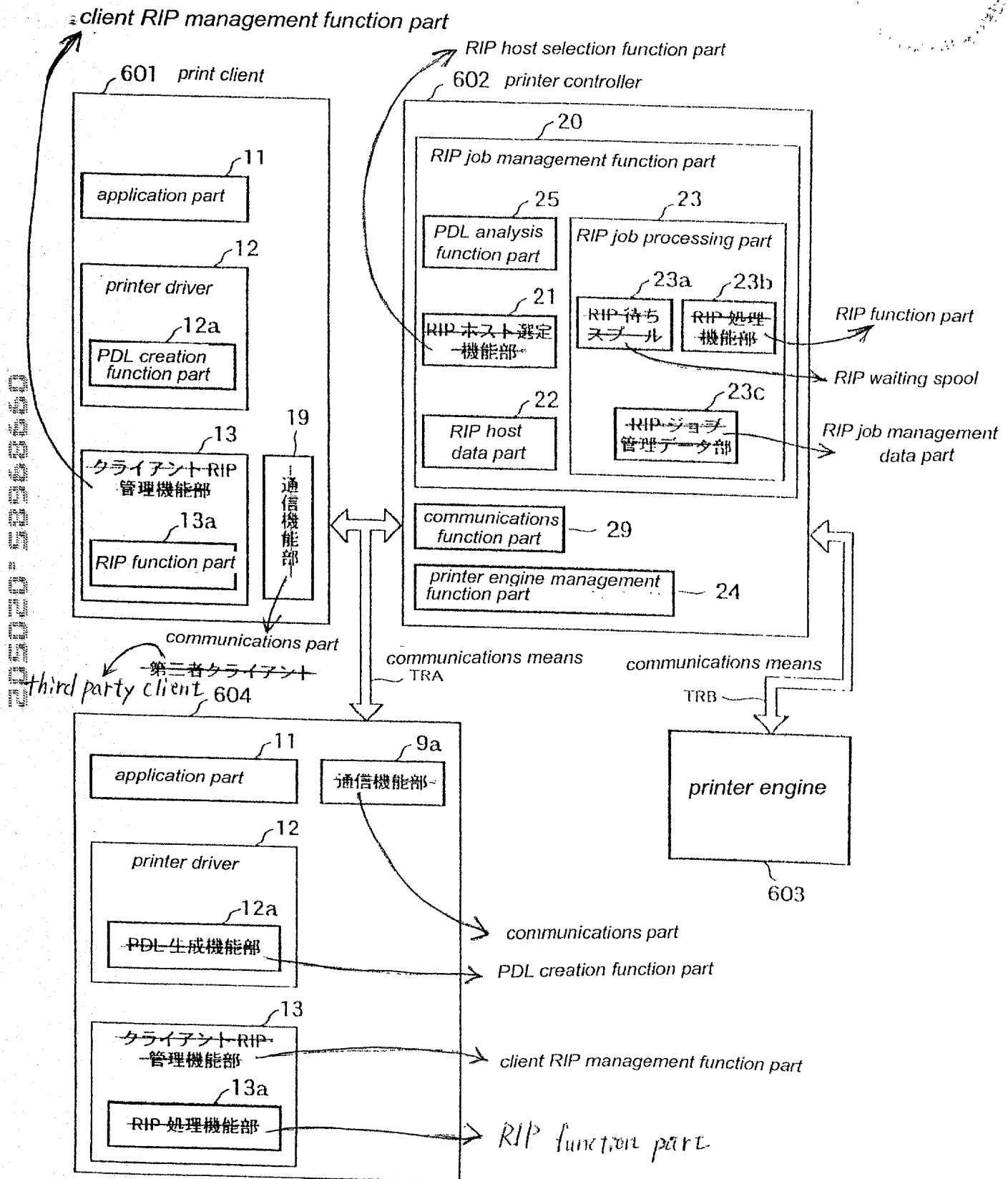


Fig. 26

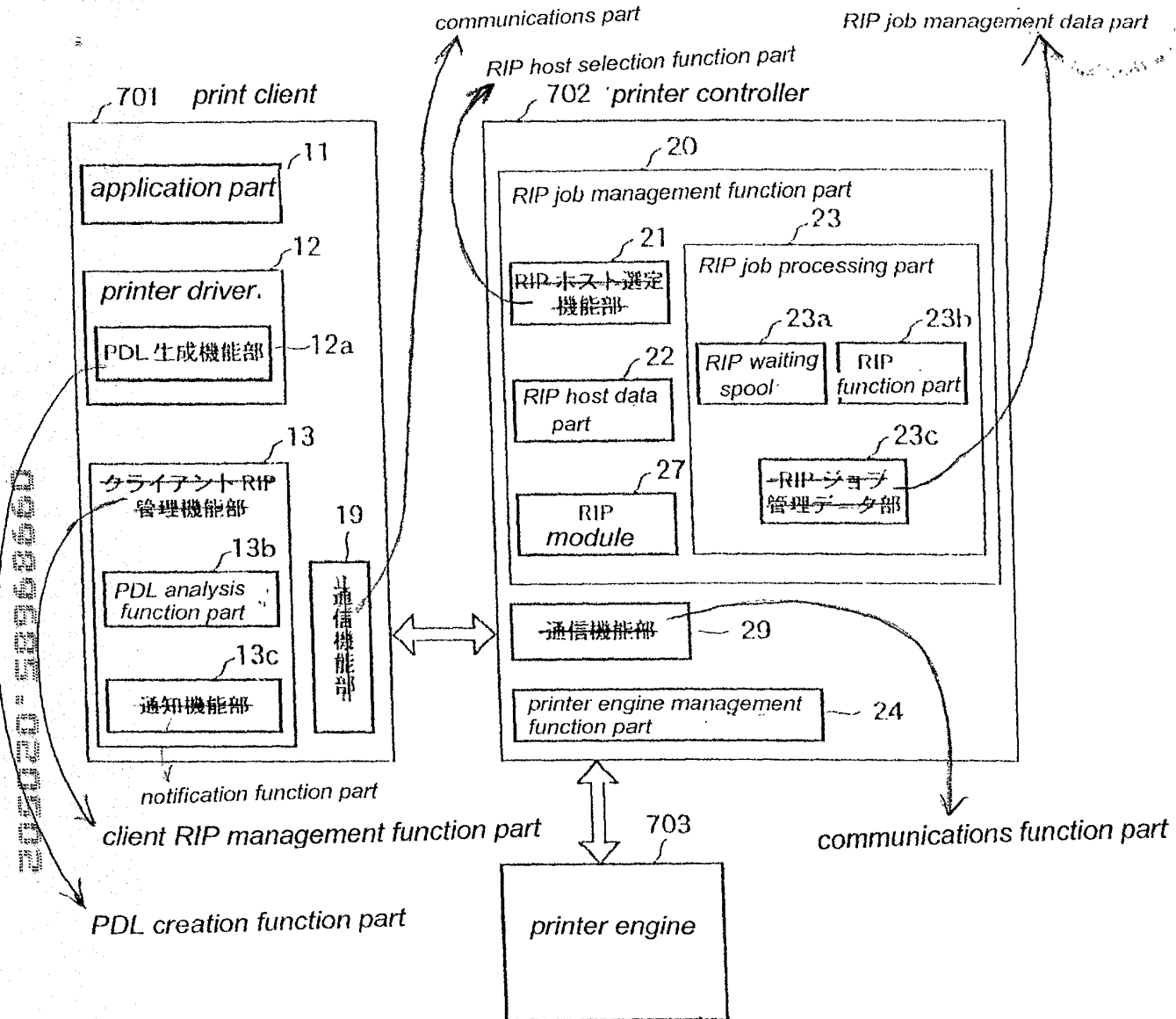
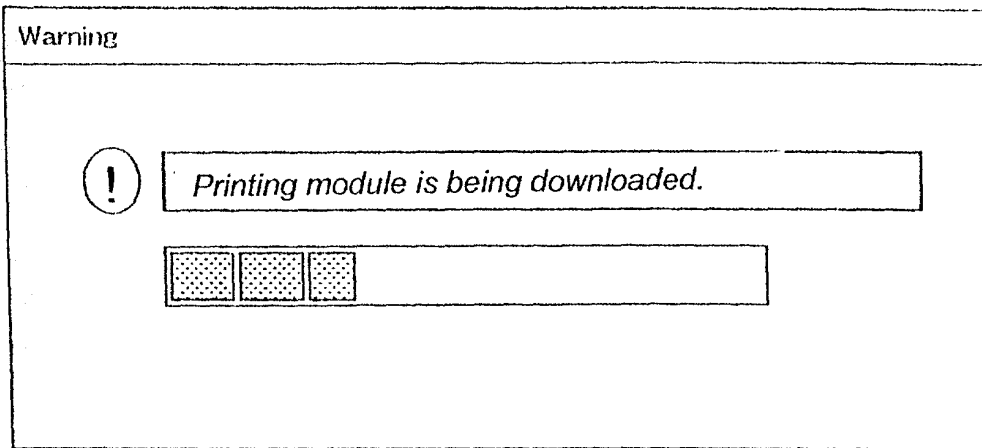


Fig. 27



205070-59968560

Fig. 28

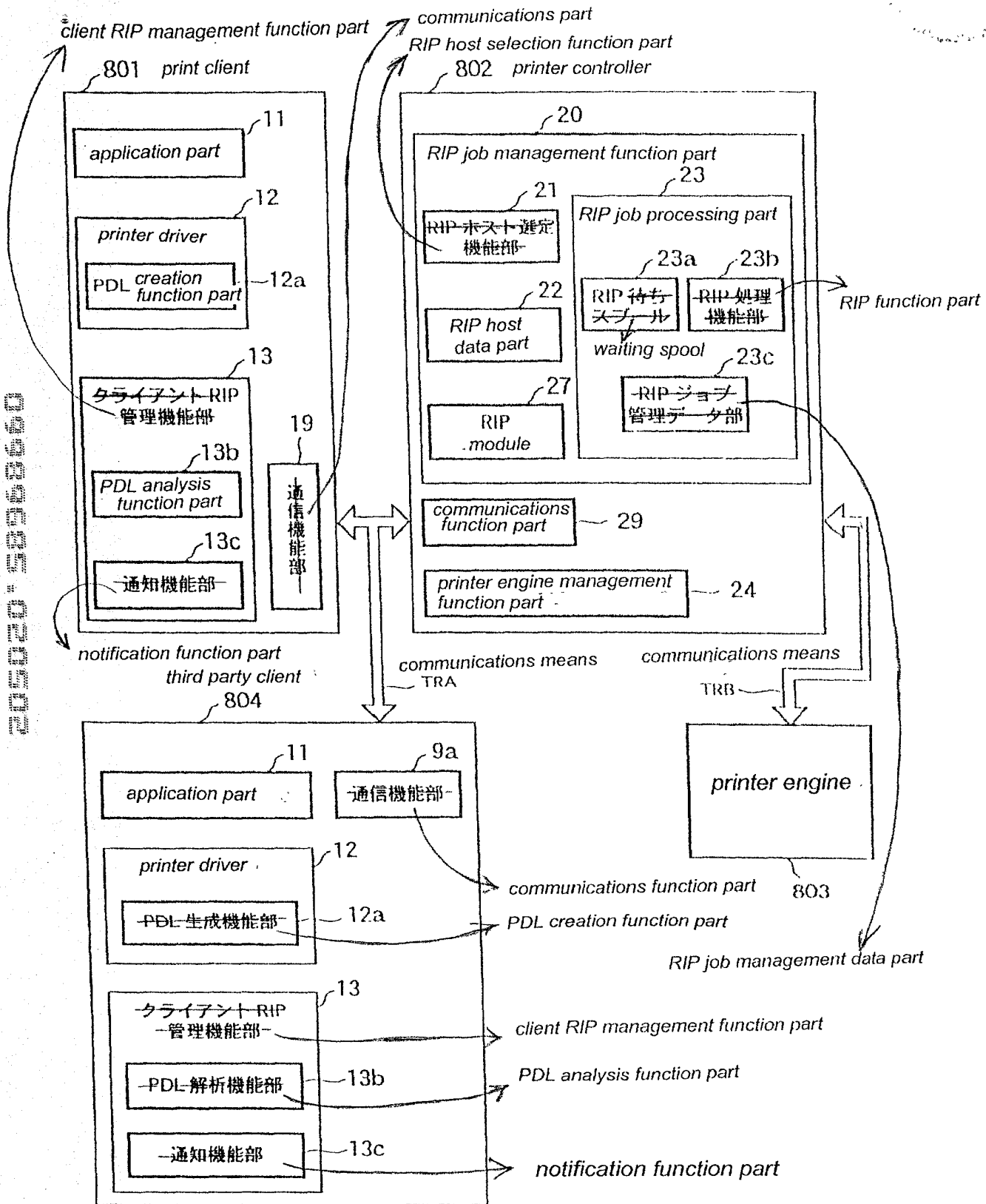


Fig. 29

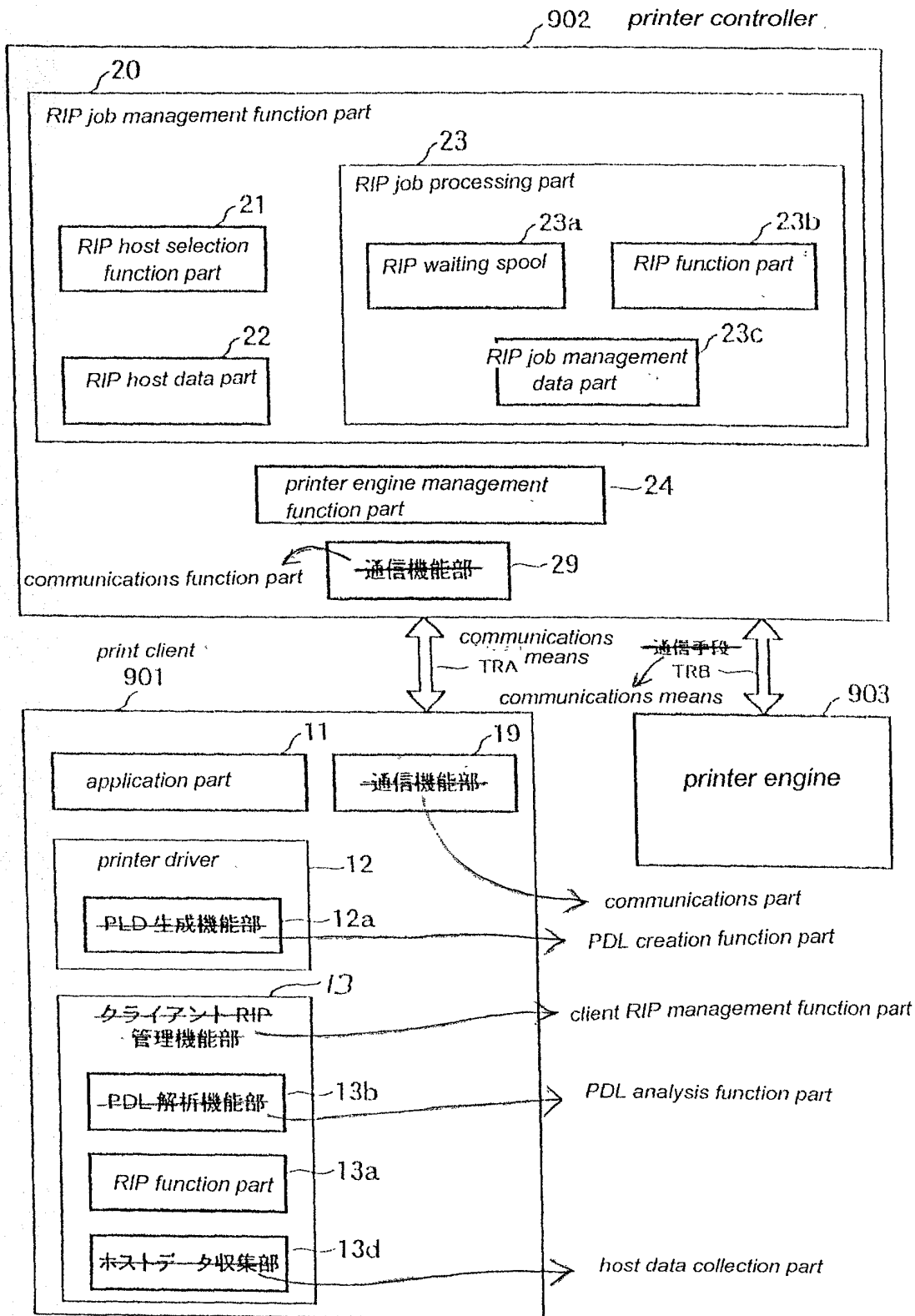


Fig. 30

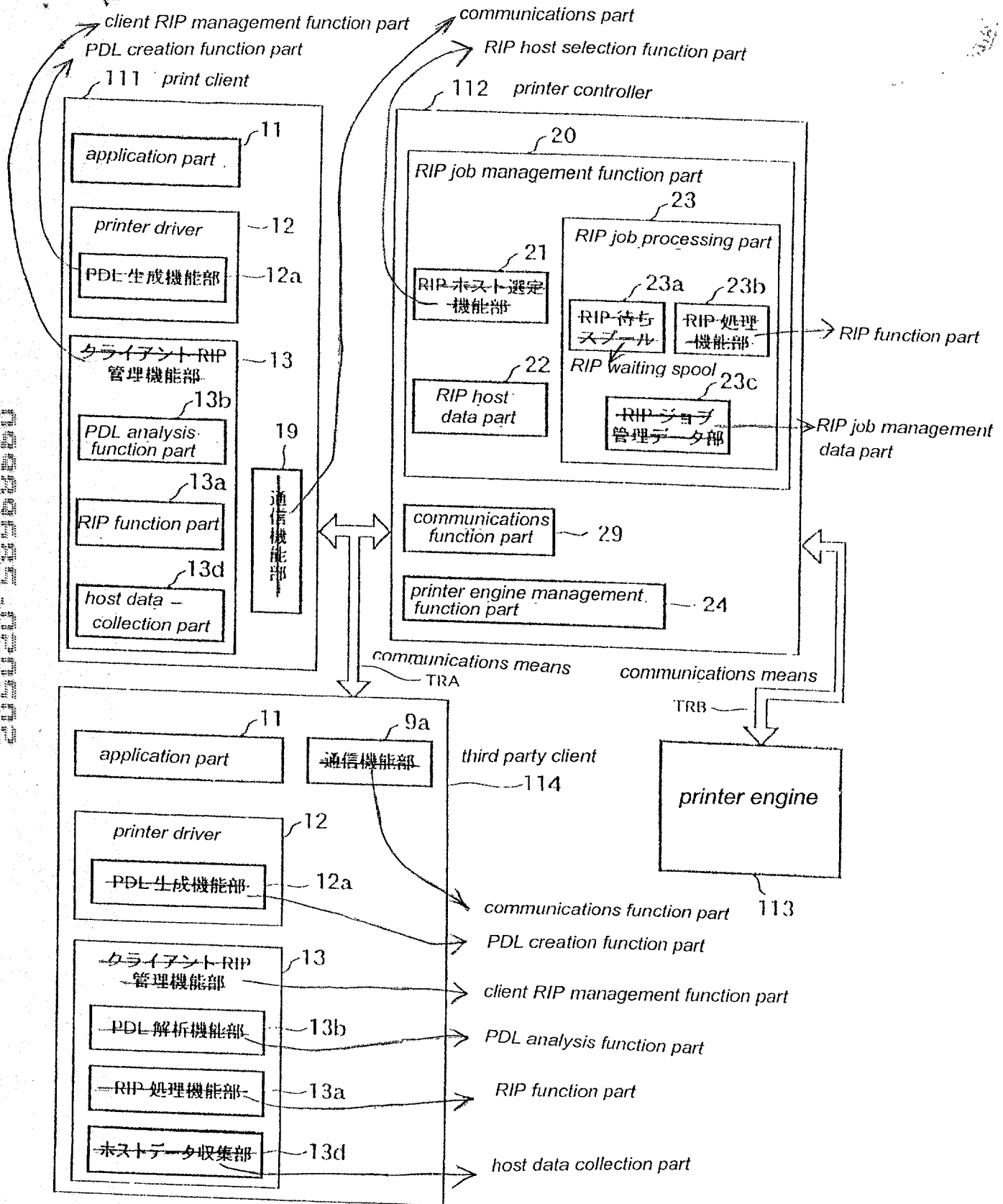


Fig. 31

$$X = (\lambda \times (\text{empty memory capacity } (xi)) + \mu \times (\text{clock frequency } (xi))) \div (\text{number of processes in running state} + (\text{number of executable processes } (xi) + 1))$$

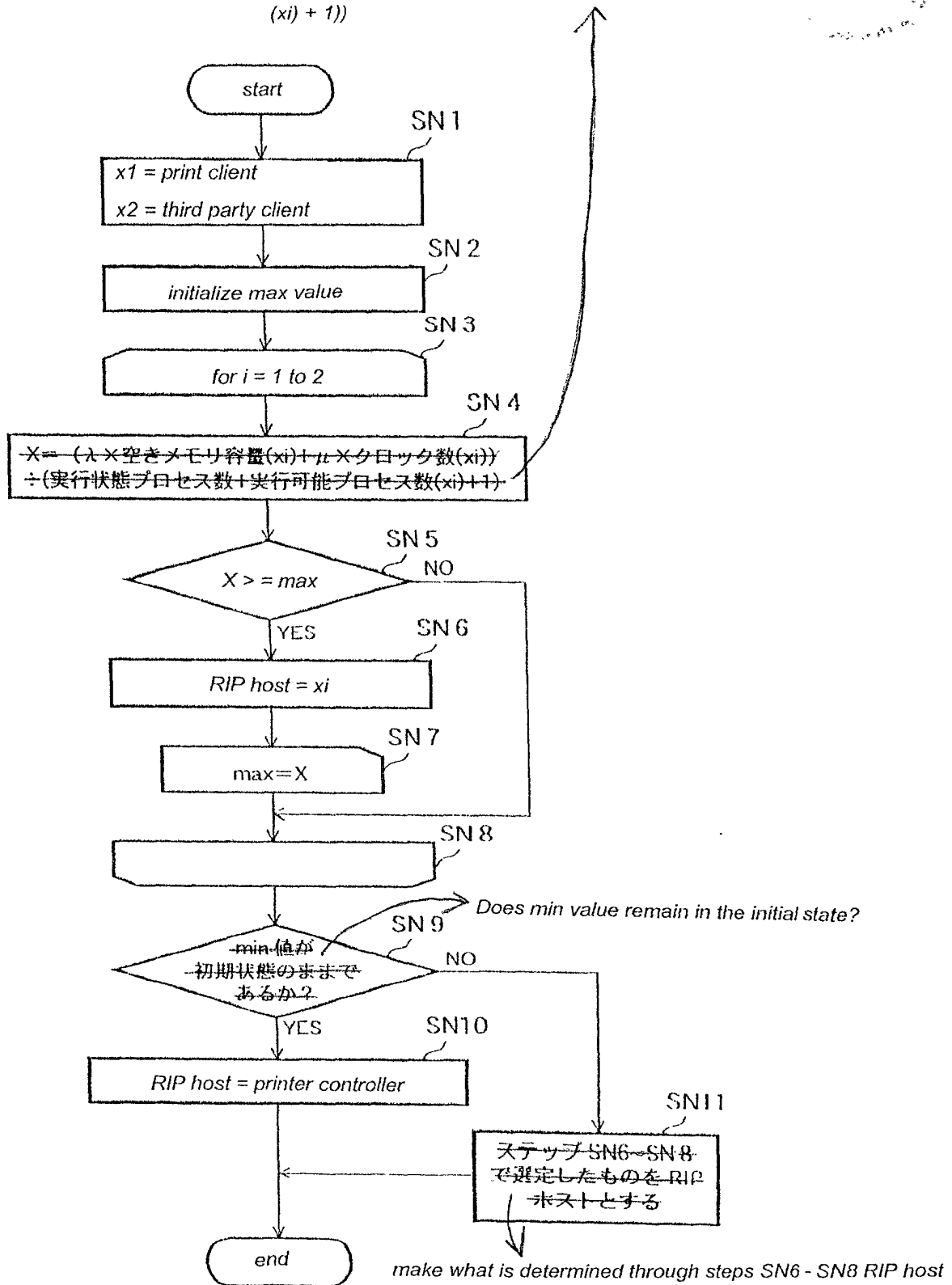


Fig. 32

$$X = (\text{RIP estimation time (xi)}) + (\text{data transmission time (xi) to printer controller})$$

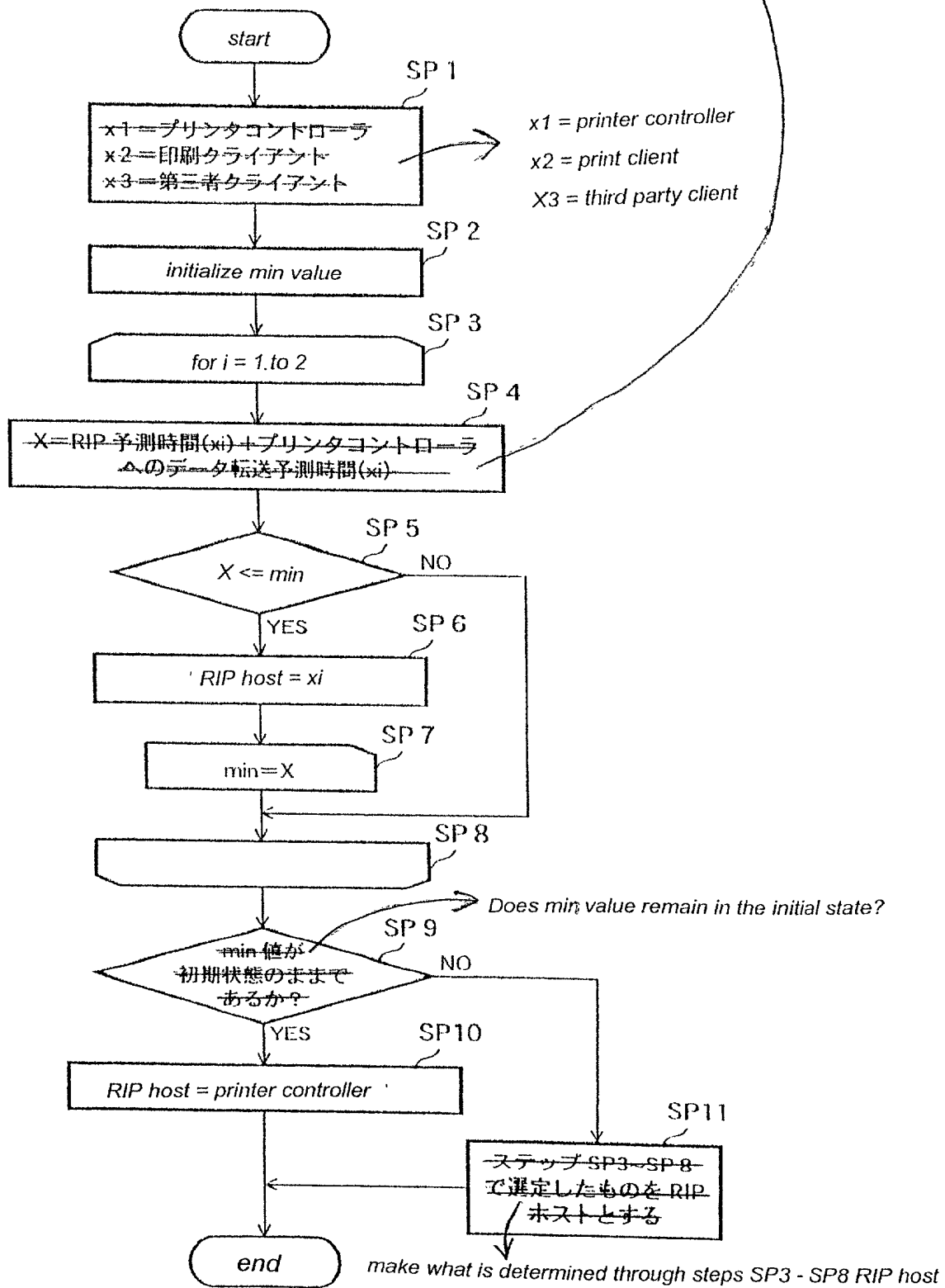


Fig. 33

